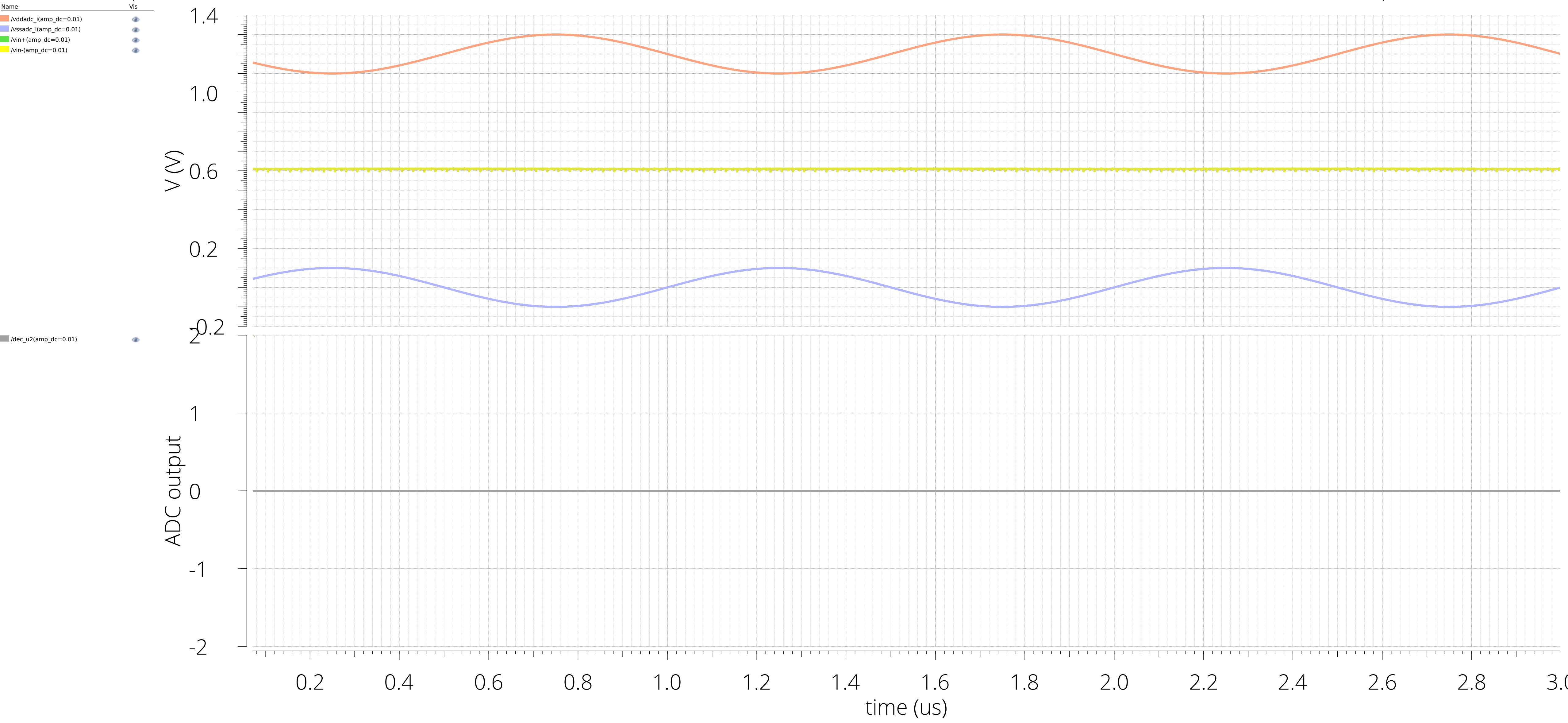


Transient Response

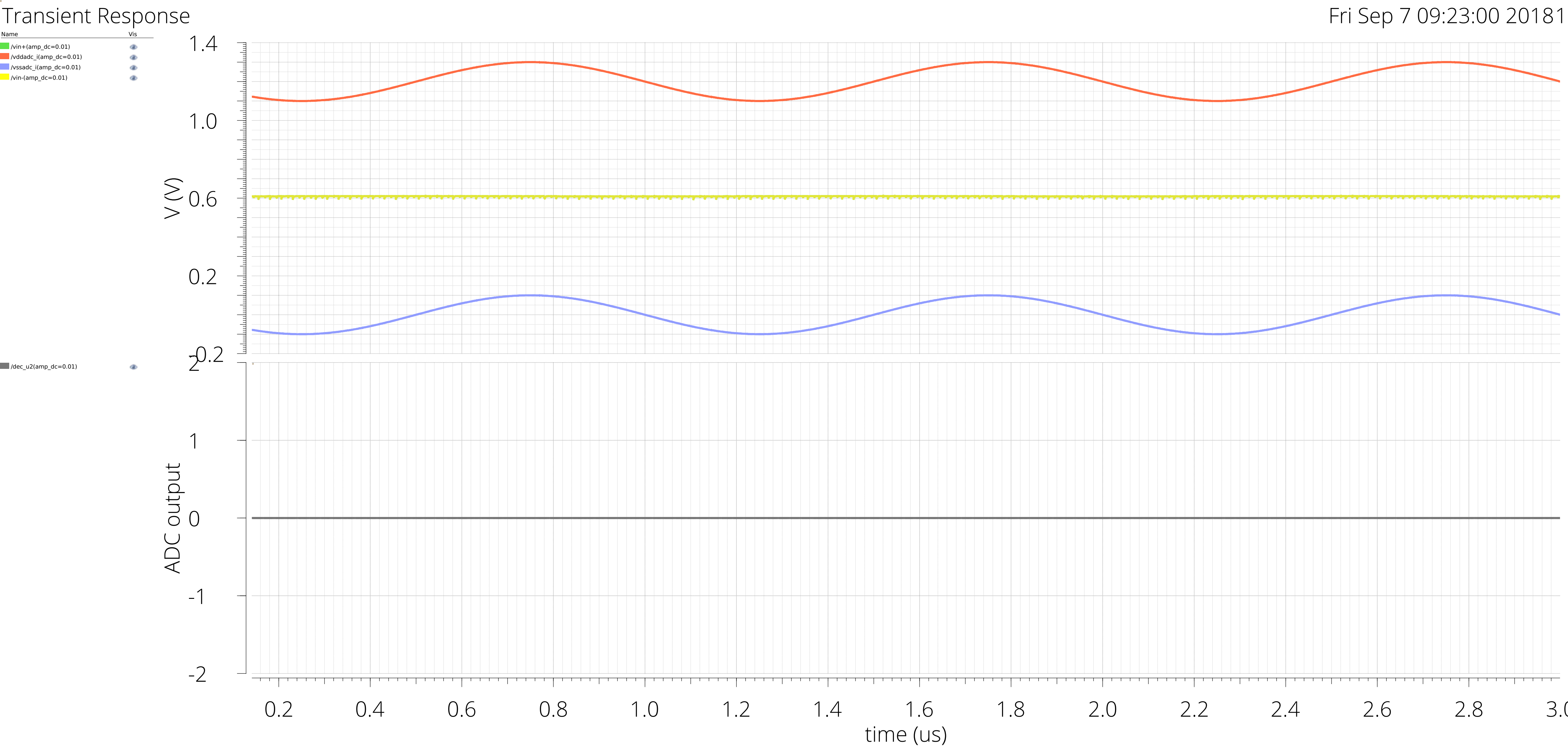
Fri Sep 7 09:16:38 2018



Transient Response

Fri Sep 7 09:23:00 20181

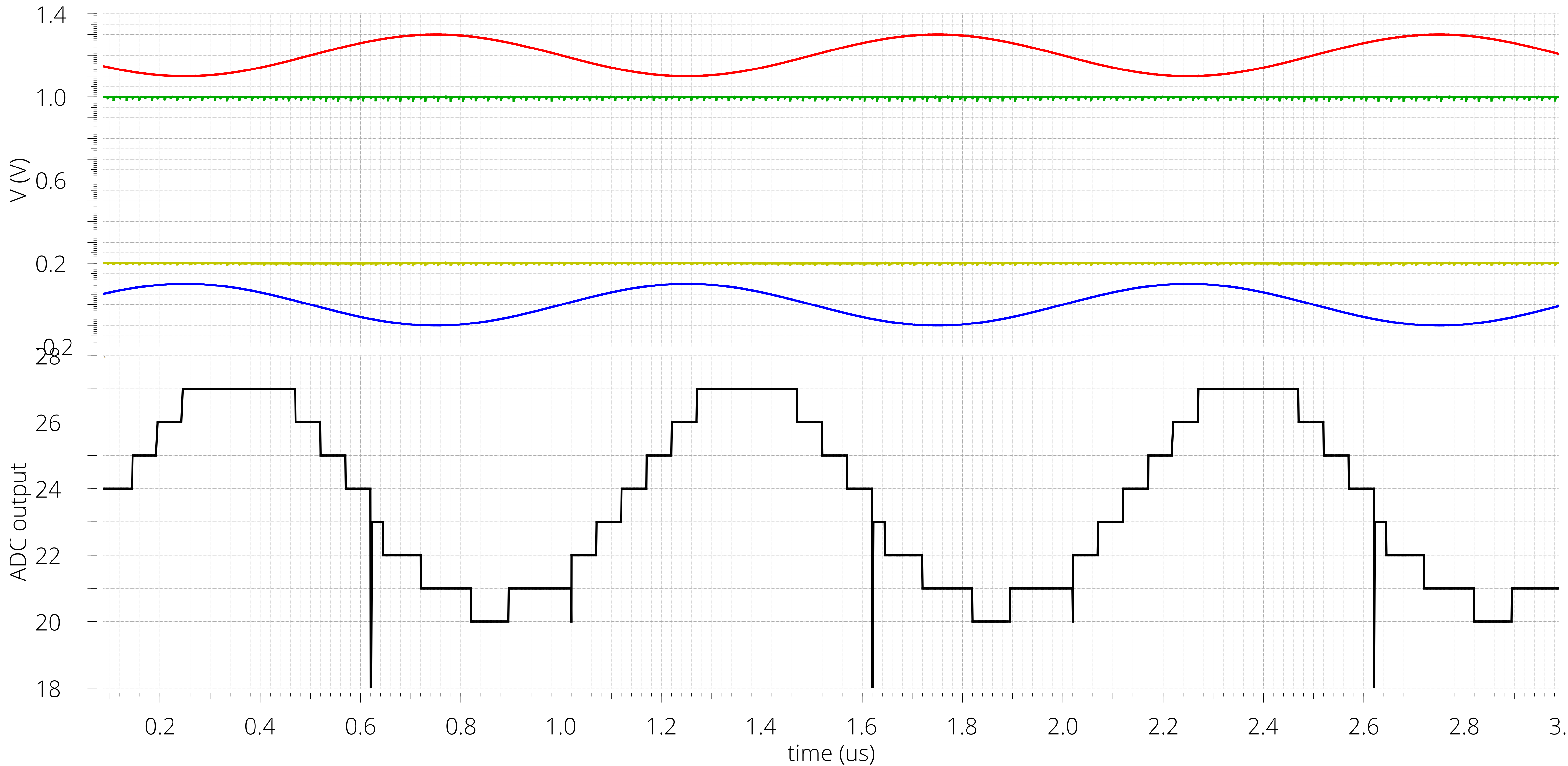
Name	Vis
/vin+(amp_dc=0.01)	☒
/vddadc_i(amp_dc=0.01)	☒
/vssadc_i(amp_dc=0.01)	☒
/vin-(amp_dc=0.01)	☒
/dec_u2(amp_dc=0.01)	☒



Transient Response

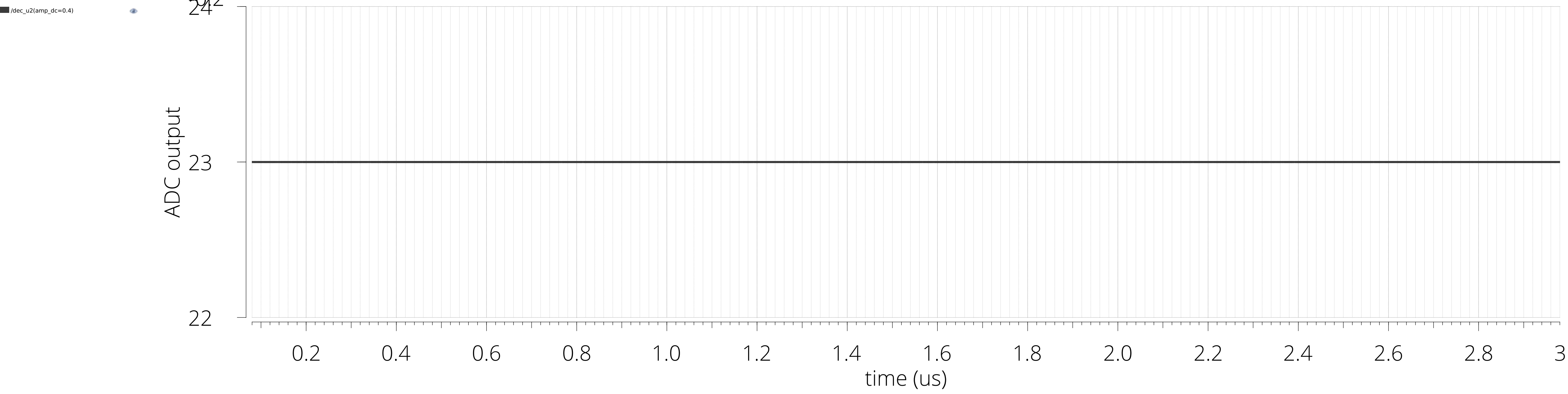
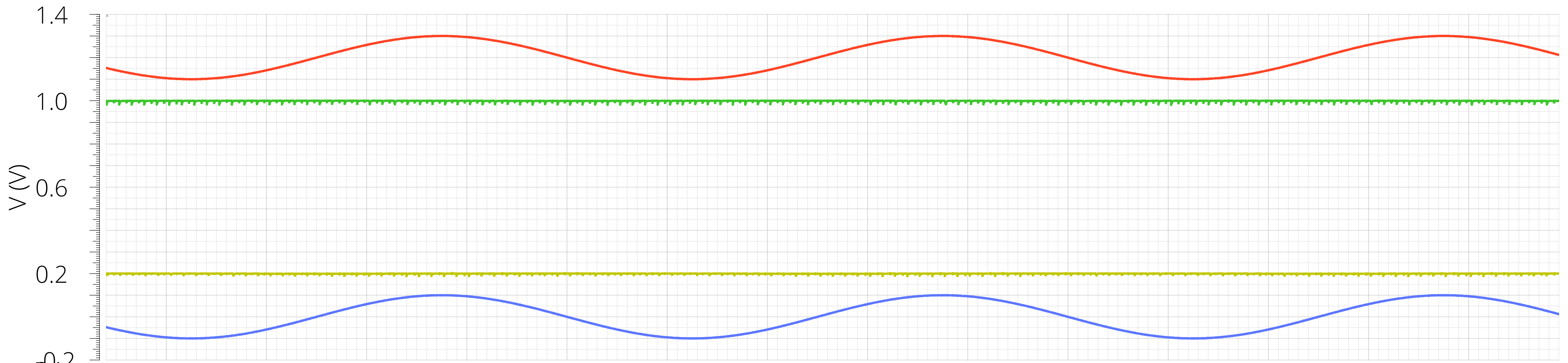
Fri Sep 7 09:52:32 20181

Name	Vis
/vddadc_i	☒
/vssadc_i	☒
/vin+	☒
/vin-	☒
/dec_u2	☐



Transient Response

Fri Sep 7 09:49:45 2018 1



Transient Response

Fri Sep 7 09:52:32 2018 1

Name Vis

/sample_clk

Vis

Vis

V(V)

/dec_u2

Vis

ADC output

time (us)

